1 2 3 5/29/12 E-FILED on 4 5 6 7 IN THE UNITED STATES DISTRICT COURT 8 FOR THE NORTHERN DISTRICT OF CALIFORNIA 9 SAN JOSE DIVISION 10 UNITED STATES OF AMERICA, No. CR-05-00812 RMW 11 Plaintiff, 12 FINDINGS OF FACT, ANALYSIS AND VERDICT (UNREDACTED VERSION)¹ 13 v. 14 SUIBIN ZHANG, Defendant. 15 16 17 18 This case was tried by the court sitting without a jury with evidence received from October 19 24, 2012 through November 1, 2012 and with final argument heard on November 11, 2012. Both 20 parties waived a jury. The court now renders its verdict based upon its findings of fact and 21 conclusions of law. 22 I. CHARGES 23 Defendant Suibin Zhang ("Zhang") is charged with three counts of computer fraud in violation of 18 U.S.C. §§ 1030(a)(4) and 1030(c)(3)(A) (Counts One through Three); three counts of 25 26 ¹ Marvell has filed a response to the court's request that it identify any portion of the court's Findings of Fact, Analysis and Verdict that it contends reveals a Marvell trade secret. By a filing 27 dated May 17, 2012 Marvell advised that "Marvell does not request that any portions of the Verdict remain under seal." Accordingly, this unredacted version of the court's Findings of Fact, Analysis and Verdict is filed in the court file available to the public. 28 FINDINGS OF FACT, ANALYSIS AND VERDICT

FINDINGS OF FACT, ANALYSIS AND VERDICT No. CR-05-00812 RMW

theft of trade secrets in violation of 18 U.S.C. § 1832(a)(1), (2) and (4) (Counts Four through Six); one count of unauthorized copying of trade secrets in violation of 18 U.S.C. § 1832(a)(1), (2) and (4) (Count Seven), one count of unauthorized transmission of trade secrets in violation of 18 U.S.C. § 1832(a)(2) and (4) (Count Eight); and one count of unauthorized possession of stolen trade secrets in violation of 18 U.S.C. § 1832(a)(3) and (4)(Count Nine). All the alleged trade secrets belong to Marvell Semiconductor, Inc. ("Marvell"), a supplier of semiconductor chips to Netgear, Inc. ("Netgear"). The government charges that Zhang, while still employed as a Project Engineer at Netgear but after he had accepted a job offer from Broadcom Corporation ("Broadcom"), misappropriated Marvell trade secrets to which he had access in his position at Netgear.

II. FINDINGS OF FACT

- 1. Broadcom is a corporation based in San Jose, California that designs and markets semiconductor chips for broadband communication devices.
- 2. Marvell is a corporation based in Sunnyvale, California that designs and markets semiconductor chips for broadband communication devices. Broadcom and Marvell are fierce competitors.
- 3. Netgear is a corporation based in San Jose, California that offers networking products for home offices and small and medium sized businesses. Netgear provides its product designs and specifications on a limited basis to technology suppliers, including Broadcom and Marvell, that supply Netgear with customized products such as switches and transceivers, which Netgear incorporates into its networking devices. Although Netgear's committed projects as of April of 2005 did not include a chassis solution for large networks, Netgear's small/medium business group was investigating the technical feasability and cost of offering such a product.
- 4. At all times relevant to this case, Marvell's products included two switch families named "Link Street" and "Prestera." The Prestera switches include: DX switches which are designed for home use and small to medium-sized business networks, EX switches designed for enterprise and large business networks and MX switches designed for use in metropolitan area networks by telecommunication companies or other large businesses with networks covering large geographic areas.

- 5. (a) Marvell takes many steps to protect the confidentiality of its proprietary information. Access to Marvell's office campus is restricted. Parking lot and building access require card keys and visitors must register and agree to a non-disclosure agreement when entering a building. Visitors must be escorted at all times when on the campus. Some floors in the buildings have additional card key only access.
- (b) Marvell maintains a restricted access extranet ("Marvell Extranet"). The Marvell Extranet is a private network that uses the Internet to share certain business information with suppliers, vendors and customers. The Marvell website serves as a document repository which enables authorized businesses to have controlled access to Marvell's proprietary product information. Marvell's website advises that the use the Marvell Extranet requires acceptance of a Non-Disclosure Agreement ("NDA"), because the information provided within the site represents Marvell's proprietary information. The NDA requires Marvell Extranet users to maintain the confidentiality of the information available on the site.
- (c) To gain access to the Marvell Extranet, Marvell requires applicants to complete a registration form which identifies the user and products of interest. A Product Manager at Marvell then reviews the registration application and decides what Extranet folders (categories of private information) the applicant will be allowed to view based upon the user's specific need. After an NDA is signed by the user, the user is assigned a user identification (his or her e-mail address) and a secure password. The user is then directed to the "Terms of Use" and prompted to click an "Accept" button, a requisite to entry to the site. The Terms of Use incorporate the NDA and a limited use license agreement.
- (d) Highly sensitive files available on the Marvell Extranet require an additional download password. To get such a password, the user must first e-mail Marvell and request a temporary password. Marvell then e-mails the user a unique password which is only for a particular download. The password expires after sixty minutes.
- (e) Every downloaded file incorporates a hash code which identifies the user and records the date and time of the download in an alphanumeric code. A confidential watermark is also incorporated in three locations in the downloaded file. Marvell maintains computer records

28

showing user activity on the Extranet including the name of the customer and its authorized employees, the date and time of log-ins and a description of any downloaded material.

- 6. Netgear's website describes Netgear's mission as "to be the preferred customer-driven provider of innovative networking solutions for small businesses and homes." Because Netgear's customer base is home users and small businesses, Netgear purchases products from Marvell's Link Street and DX product lines. Marvell permits certain Netgear employees to access Marvell's Extranet to review and consider such products.
- 7. Defendant Zhang began working at Netgear as a switch design engineer on August 14, 2000. He was promoted to Product Development Manager in December of 2001 and was responsible for Ethernet hub and switch development. He had responsibility for managing the development and deployment of small and medium sized business products. On November 6, 2003 he signed Marvell's Limited Use License Agreement on behalf of Netgear, which provides in part: "Marvell hereby grants to Licensee . . . a non-exclusive non-transferable, non-exclusive, royalty-free limited license to use and, if required, modify [the software modules comprising the Marvell Prestera Software Suite, and any Marvell specified documentation thereon, provided by Marvell to Licensees] for (i) Licensee's internal evaluation in connection with development of Licensee's Products and/or Licensee's Software for use exclusively with Marvell's Products "Ex. 33 at § 2.1. Zhang, on behalf of Netgear, further agreed that "Licensee shall maintain in confidence Marvell's confidential Information in accordance with the terms of that Non-Disclosure Agreement ('NDA') most recently entered into by Licensee and Marvell or any Marvell Affiliate." Id. at § 4. Zhang acknowledged that the NDA had been renewed on August 21, 2003. The NDA provides, among other things, that "Receiver agrees not to use any Confidential Information for any purpose except to evaluate and engage in discussions concerning a potential business relationship between Company and Marvell" (Exs. 22 at ¶ 3; 34 at ¶ 3); "Receiver agrees that it shall take all reasonable measures to protect the secrecy of and avoid disclosure and unauthorized use of Confidential information" (id. at ¶ 4); "All documents . . . containing or representing Confidential Information and all copies thereof in the possession of Receiver . . . shall be promptly returned to Discloser upon Discloser's request or upon the termination of this Agreement. *Id.* at ¶ 7. Confidential information

is broadly defined by the NDA and required to be marked as "Confidential," 'Proprietary' or the substantial equivalent thereof." *Id.* at 2. Zhang had default access to the Marvell Extranet as Netgear's Product Manager. He was given a user name and password. Zhang knew that Netgear was governed by Marvell's Extranet Terms of Use and that he had to comply with those terms. *See* Ex. 21. Zhang was personally governed by Netgear's Employee Invention Assignment and Proprietary Information Agreement. Ex. 38. Although a copy of that agreement signed by Zhang apparently could not be found by Netgear, the evidence was highly persuasive that he had, in fact, signed it. Exhibit 39 lists Zhang as a signatory and Wylea Kirkpatrick, who worked in the personnel office at the time, confirmed she had seen a copy of the agreement that Zhang signed. Her job duties included making sure that the appropriate employees signed the Agreement.

- 8. In the Fall of 2004 and Spring of 2005, Zhang applied for employment with both Marvell and Broadcom. He interviewed at Marvell on February 14, 2005 and signed a unilateral non-disclosure agreement with Marvell which was similar to one he previously signed with Netgear. Ex. 32. On March 3, 2005 he received an employment offer from Marvell. He also received an offer from Broadcom, but it was not clear from the evidence when the offer was made or when he accepted it. On March 8, 2005 Zhang e-mailed his supervisor at Netgear and indicated that he had accepted a job at Broadcom in the area of strategic product planning and intended to leave Netgear by the end of March 2005. On March 10, 2005 Zhang also advised those with whom he had interviewed at Marvell that he was not taking Marvell's offer of employment. He told Marvell that "I will remain at Netgear for a few weeks for the transition and continue to drive existing committed projects for delivery. To avoid any conflict of interests, I would excuse myself from involving chipset decisions for new projects." Ex. 31. Zhang agreed to extend his last day at Netgear to April 15, 2005 because Netgear was having difficulty finding someone with the qualifications necessary for the position Zhang had. The evidence is unclear as to what work Zhang actually did for Netgear after he indicated he had accepted a position with Broadcom.
- 9. (a) On March 9, 2005 Zhang downloaded the following information from the Marvell Extranet using the authorization he had for access:

DATASHEET- 88E6185, 88E6182, 88E6155, 88E6152, Gigabit Ethernet Switch

| 1 | RD-88E6095-48F4G Reference Design Schematics |
|----|---|
| 2 | 48FE +2GE Managed and Stacked System Hardware System Design Specification |
| 3 | RD-88E6095-48F4G Design and Layout Guidelines |
| 4 | RD-88E6095-48F4G Reference Design BOM |
| 5 | RD-88E6095-48F4G Gerber Files |
| 6 | SOHO Switch Software Architecture Specification |
| 7 | SOFTWARE - SOHO Switch Drivers DSDT Ver 2.3b |
| 8 | WHITE PAPER - Auto-Neg/MAC Interface |
| 9 | WHITE PAPER - Auto-Neg/MAC Interface |
| 10 | WHITE PAPER - Back-Pressure for Flow Control |
| 11 | WHITE PAPER - Common Mistakes with Ethernet System |
| 12 | WHITE PAPER - Flow Control Explained |
| 13 | WHITE PAPER - How to Quote, Measure and Use Power |
| 14 | WHITE PAPER - Supply Noise Measurement |
| 15 | WHITE PAPER - Tutorial on Clock PPM Effects |
| 16 | WHITE PAPER - Tjc, Tja & Temperature Calculations |
| 17 | Bailout for Prestera 98DX270 and 98DX273 |
| 18 | Hardware Design Guide for Prestera 98DX250/253/260/263/270/273 |
| 19 | Hardware Specification for Prestera 98DX250/253/260/263/270/273 |
| 20 | Product Brief - 98DX253/263/273 |
| 21 | Functional Errata and Restriction for DX166/166R/246/250/260/270 |
| 22 | FUNCTIONAL SPECIFICATION & REGISTER SET FOR |
| 23 | 98DX166/166R/167/167R/246/247/250/253/260/263/270/273/803 |
| 24 | BSDL Model for 98DX273 Rev AI |
| 25 | IBIS Model for 98DX250/253/260/262/263/270/273/803 Rev A1 |
| 26 | Bailout for Prestera 98DX803 |
| 27 | Hardware Specification for Prestera 98DX803 |
| 28 | Product Brief- Prestera 98DX803 |

| 1 | FUNCTIONAL SPECIFICATION & REGISTER SET FOR |
|--------|--|
| 2 | 98DX166/166R/167/167R/246/247/250/253/260/263/270/273/803 |
| 3 | BSDL Model for 98DX803 Rev AI |
| 4 | IBIS Model for 98DX250/253/260/262/263/270/273/803 Rev Al |
| 5 | Bailout for 98EX126-A |
| 6 7 | Hardware Specification - Prestera 98EX116, 98EX126 and 98EX136 Packet Processors |
| 8 | FUNCTIONAL ERRATA- PRESTERA PACKET PROCESSOR 98EX116-N98EX126-A |
| 9 | (b) On March 16, 2005 Zhang downloaded the following information from the |
| 10 | Marvell Extranet: |
| 11 | Ballout for 98EX126-A |
| 12 | APPLICATION NOTE - AN-171 Policy and Meter Configuration Guidelines |
| 13 | APPLICATION NOTE - AN-146-FC and HOL Configuration for Prestera Devices |
| 14 | APPLICATION NOTE - AN-148 Implementing a GbE Desktop Value-Added |
| 15 | Solution using the Prestera 98DX240 and 98EX126 Devices |
| 16 | APPLICATION NOTE- Prestera Hardware Design Guide for EX/FX/MX |
| 17 | APPLICATION NOTE - AN-126 Thermal Management and Power consumption |
| 18 | for the Prestera Packet Processor |
| 19 | APPLICATION NOTE- an-118 ppm Differences Between Rx Clock and Tx Clock |
| 20 | APPLICATION NOTE - AN-I 02 Prestera Systems Power Sequencing |
| 21 | APPLICATION NOTE - AN-109 1Pv4 Routing Engine Data Structure Memory |
| 22 | Consumption in the Prestera-EX Packet Processor Family |
| 23 | APPLICATION NOTE- AN-122 Broadcast and Multicast Rate Limit Configuration |
| 24 | APPLICATION NOTE- AN-151 Implementing Auto-Media Detect in 88E llll/4x |
| 25 | Transceivers with Prestera EX/MX Packet Processors |
| 26 | APPLICATION NOTE- AN-67 Powering Up/Powering Down Marvell. Devices with |
| 27 | Multiple Power Supplies of Different Voltages |
| 28 | APPLICATION NOTE - AN-79 Implementing Nested VLANs Using the Prestera |

| 1 | Devices, 98EX and 98MX Family |
|----|---|
| 2 | APPLICATION NOTE - AN-80 Prestera Packet Processor Memory Usage |
| 3 | APPLICATION NOTE - AN-89 Back-end Port Configuration in Prestera Packet Processors |
| 4 | Application Note - Value Blade Software Architecture |
| 5 | DB-98EX126-12G750 BOM |
| 6 | DB-98EX126-12G750 Schematics DB-98EX126-12G750 User Manual |
| 7 | EV-64260A-750CX-PCI User Guide |
| 8 | WxWorks BSP User Manual for EV-64260A-750CX-PCI |
| 9 | |
| 10 | (c) On March 18, 2005 Zhang downloaded the following documents from the Marvell Intranet: |
| 11 | |
| 12 | DATASHEET- 88Eil49 Quad-Channel Gigabit Ethernet PHY |
| 13 | APPLICATION NOTE - 88E1 149 PCB Layout Guidelines |
| 14 | APPLICATION NOTE - Alaska MDI Interface Design |
| 15 | APPLICATION NOTE - Measuring Supply Noise |
| 16 | APPLICATION NOTE - Oscillator References for Gigabit PHYs |
| 17 | APPLICATION NOTE- Using Marvell PHYs in Backplane Applications |
| 18 | APPLICATION NOTE - Crystal Oscillators |
| 19 | APPLICATION NOTES - Differential Single Impedance Calculations |
| 20 | SPREADSHEET |
| 21 | APPLICATION NOTES - Alaska Gigabit Magnetics |
| 22 | APPLICATION NOTES - Alaska Gigabit Magnetics |
| 23 | MODELS - 88E1149-BAM(BGA) BSDL Model |
| | MODELS - 88El149-TAH (TQFP) BSDL Model |
| 24 | WHITE PAPER - How to Quote, Measure and Use Power Numbers |
| 25 | WHITE PAPER - BER Vs. Packet Loss |
| 26 | WHITE PAPER - Auto-Neg/MAC Interface |
| 27 | WHITE PAPER - Auto-Crossover & Parallel Detect |
| 28 | |

DATASHEET - 98MX6x5-A

APPLICATION NOTE - AN-143 Controlling Traffic to the CPU for 98EX1xx - C/D and 98MXxx Prestera Packet Processor

- 10. (a) The volume of these three downloads contrasts markedly with the total of nine documents Zhang had downloaded from the Extranet during the fourteen month period before March 2005. There is no evidence that he needed any of the materials from the three March downloads to "drive existing committed projects for delivery."
- (b) The downloaded documents dealt with a broad range of Marvell products and contained what Marvell considered confidential, proprietary information. Exhibit 18 graphically shows Zhang's downloading activity during the course of Zhang's employment and how it ran below that of all other Netgear employees combined until March of 2005 when his downloads were many times greater than those of all other Netgear employees' combined.
- (c) The only document downloaded by Zhang from the Marvell Extranet that he shared with Broadcom or any other party is Exhibit # 28 (same as page 96 of Exhibit 5) (a page of the hardware specifications for 98 DX250/253/260/263/270/273). That page is watermarked "Marvell Confidential Under NDA#121011740" and has a footer stating "CONFIDENTIAL." Although most of the information on the document was known in the industry, it reveals electrical current consumption under extreme conditions which would be valuable to a system designer. *See* § 11 *below*.
- 11. The Government proved through the tedious, and at times highly technical, testimony of George Hervey, the senior engineer with Marvell in charge of all customer support for Marvell products, that some of the information Zhang downloaded had independent value from not being generally known to, and not being readily ascertainable by, the public.² Specifically, Hervey discussed Government's Exhibits 1 through 16 which the Government sought to prove contain trade

² Although defendant contended that the Government failed to prove that the downloaded information derived independent economic value from not being generally known to the public, he offered limited evidence to contest Hervey's testimony that the information was valuable. He focused more on challenging the Government's claim that Marvell took reasonable measures to keep such information secret.

secret information. Although Hervey testified that much information contained in those exhibits was available to the public, the combination of the information in each exhibit showed Marvell's product design choices and how the features of its products were implemented. This information is carefully guarded by Marvell and would be of value to a competitor who could use the information in making its own design choices or it could use the information to design tests that would exploit weaknesses in competing Marvell products and cause them to perform poorly under particular circumstances. Hervey also explained that certain pieces of information within the exhibits by themselves have independent value from not being generally known to, or readily ascertainable by, the public. The following table lists: (1) the sixteen exhibits that the Government introduced, (2) the nature of the asserted economic value from not being publically known of both the compendium of information in the exhibit and of certain information on individual pages from the exhibit, (3) what warning the exhibit bears indicating it is confidential and (4) the date of Zhang's download.

Where the Government failed to sustain its burden of showing, for each piece of information, the economic value of not being generally known to the public, the court explains its reasoning in parentheses below the description of the information. Where the Government sustained its burden, the court either simply describes the information or explains why the burden was met without using parentheses.

| Alleged Trade Secret | Description of Information and Alleged Economic Value from Not Being Generally Known to the Public | Indications on Document That It Is to Be Kept Secret | Date When Zhang Downloaded Document |
|---------------------------|--|--|--|
| Government Exhibit 1: | Compendium of information | Watermark | 3/9/2005 |
| Link Street TM | | and footer | |
| 88E6152/88E6155/, | For Marvell's Link Street chip | state | |
| 88E6182/88E6185 | family, compendium of information | "confidential" | |
| Datasheet | shows the functional description of the | | |
| | device, the interfaces on the device, the | | |
| Specific pages that | electrical power needed to power the | | |
| contain purported | device and specific design choices | | |
| trade secret | made by Marvell. Some of the design | | |
| information: | choices continue to be used in | | |
| | Marvell's new generation of products. | | |
| a. pg. 6: | Public disclosure of information | | |
| 88E6152/88E6155/88 | would, among other things, allow | | |
| E6182/88E6185 | competitors to take advantage in the | | |
| Device Differences; | marketplace of how the multiple | | |
| | features of Marvell's product share the | | |

FINDINGS OF FACT, ANALYSIS AND VERDICT No. CR-05-00812 RMW

| 1 | b. pg. 69: Format of | same implementation. Has independent | |
|----|--|---|--|
| 2 | the Address Translation Unit | value from not being generally known because it has a design and | |
| 3 | Database; | implementation that competitors do not have. | |
| | c. pg. 95: Describes | | |
| 4 | the distributed switching architecture; | <u>Specific pages containing purported</u> trade secret information | |
| 5 | | | |
| 6 | d. pg. 176: Shows a register and describes | a. Shows differences between the four devices within the Link Street family. | |
| 7 | a method of gathering statistics from the | What features are enabled for each device is based on Marvell's | |
| | device. | knowledge of the industry and | |
| 8 | | understanding of the market. (Hervey's testimony did not establish | |
| 9 | | beyond a reasonable doubt that Marvell's knowledge of the industry | |
| 10 | | and market was not known by the | |
| 11 | | public and thus had independent economic value from not being known | |
| 12 | | by the public). | |
| | | b. Shows exact format of forwarding | |
| 13 | | databases, revealing some features that are not traditionally present in a | |
| 14 | | forwarding table such as priority indication, and Marvell's unique design | |
| 15 | | choices such as field multiplexing for | |
| 16 | | reducing memory footprint. | |
| 17 | | c. Describes Marvell's implementation of the distributed switching architecture | |
| | | (DSA), which enables two or more | |
| 18 | | devices to behave as a single device. Although the idea of making two | |
| 19 | | devices behave as one by setting particular TAG field is generally | |
| 20 | | known in the industry, the specific bit | |
| 21 | | contents and meaning of Marvell's 4-byte TAG allows it to achieve certain | |
| 22 | | features traditionally difficult to do across chips. | |
| | | • | |
| 23 | | d. Describes a register associated with the function of gathering statistics from | |
| 24 | | the device, and the different status of the register bits, which are used to | |
| 25 | | control different operations the device | |
| 26 | | performs. The description shows that Marvell's device enables a CPU to | |
| 27 | | collect statistical data from all ports at once, besides collecting them from | |
| 28 | | individual port. | |
| 40 | | (Hervey testified that this specific | |

| 1 | | register design allows Marvell to use low-end CPUs to perform the | | |
|----|---|---|------------------|----------|
| 2 | | statistics-gathering function, thereby reducing the cost of the system. | | |
| 3 | | However, page 176 makes no reference to low-end CPUs, nor does it teach how | | |
| 4 | | to implement the function with low-end | | |
| 5 | | CPUs. Without more, the evidence does not establish beyond a reasonable | | |
| 6 | | doubt that a competitor would necessarily conclude that the purpose | | |
| 7 | | of this register design is to allow the use of low-end CPUs. Accordingly, it is | | |
| 8 | | not clear that information on this page alone derives independent economic | | |
| 9 | | value from not being publically available.) | | |
| 10 | Government Exhibit 2: | Compendium of information | Watermark | 3/9/2005 |
| 11 | SOHO Switch Software Architecture | Contains software architecture | and footer state | |
| 12 | Specification | specifications for certain Marvell small office/home office Ethernet switches, | "confidential" | |
| 13 | Specific pages that contain purported | which allow customers to develop software on top of Marvell's device | | |
| 14 | trade secret information: | drivers. | | |
| 15 | a. pg. 80, section | Specific pages containing purported trade secret information | | |
| 16 | 6.2.1.11: definition of Marvell's API | a. This is a definition of Marvell's | | |
| 17 | (application program interface), which | Application Interface Layer (API) that gives insight into the maximum, | | |
| 18 | defines how to set the multi cast rate limits; | minimum, etc. number of bits per second that the packet flows through. | | |
| | , | The information would enable a | | |
| 19 | b. pg. 140, section 6.2.5: API for setting a | competitor to obtain these number limits that Marvell set and target these | | |
| 20 | field for the port association vector. | limits in their own design to build a more superior chip. | | |
| 21 | | b. This is an API for setting a field | | |
| 22 | | called port association vector. Implementing three unique features | | |
| 23 | | using a single port association vector, instead of three separate fields, is | | |
| 24 | | considered a trade secret by Marvell, as | | |
| 25 | | this implementation decision is made based on Marvell's understanding of | | |
| 26 | | the target market and their experience with customers. | | |
| 27 | | (Hervey testified that he did not know whether this implementation is | | |
| 28 | | common within the industry, and thus the government did not show that the | | |

| 1 | | information was a trade secret.) | | |
|----------|---|---|--|----------|
| 2 | Government Exhibit 3: | | No confidential | 3/9/2005 |
| 3 | 2.3a | accompanying the actual driver software that Marvell provided to customers. The release note is part of a | confidential marking on the document | |
| 4 | | compressed .zip file and not separately downloadable from the Marvell | itself. However, not | |
| 5 | | ExtraNet. It describes the changes from the previous version of software as well as a list of contents in the current | separately downloadable from | |
| 7 | | version. This document enables a competitor to know certain new | compressed .zip file. | |
| 8 | | features that were added to Marvell's devices. | 1 | |
| 9 | Government Exhibit 4: 4:98DX250/260/270, | Compendium of information | Watermark and footer | 3/9/2005 |
| 10 | 98DX253/263/273, and 98DX262 | Contains a collection of recommendations for customers who | state "confidential" | |
| 11 | Prestera TM Hardware Design Guide | design boards using Marvell's Prestera devices. It shows certain design choices | Communication | |
| 12 | Specific pages that | that Marvell made such as what type of Gigabit Port there is on the device. | | |
| 13 | contain purported trade secret | Public knowledge would help competitors to estimate the cost of | | |
| 14 | information: | using Marvell's device on a board. | | |
| 15 | a. pp.12-13, section 2.3: Capacitors; | Specific pages containing purported trade secret information | | |
| 16 | b. pg. 32, section | a. Shows Marvell's suggestions as to | | |
| 17 18 | 3.2.5.5: Lane Swapping of HyperG Stack Ports. | the size and characteristics of capacitors to be used for each power rail. This information is derived based | | |
| 19 | | on Marvell's design experience, and is applicable to Marvell's devices with | | |
| 20 | | certain type of package and interfaces. With this information, a competitor | | |
| 21 | | could derive the cost of capacitors for using Marvell's device on a board, and | | |
| 22 | | aim to design a device requiring fewer capacitors. | | |
| 23 | | (Hervey's testimony is weak regarding the independent economic | | |
| 24 | | value of the information, because even if a competitor may derive the cost of | | |
| 25 | | capacitors for using Marvell's device on a board, that cost may be trivial in | | |
| 26 | | board design because a board contains many different devices that are more | | |
| 27 | | expensive than capacitors. Therefore, the evidence introduced does not | | |
| 28 | | establish beyond a reasonable doubt that (a) has independent economic | | |

| 1 | | value from not being generally known by the public.) | | |
|----|--|--|---------------------|----------|
| 2 | | | | |
| 3 | | b. Describes an operation on Marvell's inter-chip ports that allows a customer to reduce the layers on the motherboard | | |
| 4 | | by swapping pins (positive pin becomes the negative interface). | | |
| 5 | | (The evidence that this information | | |
| 6 | | derives independent value from not being generally known is not | | |
| 7 | | sufficiently clear to allow a finding of such value beyond a reasonable doubt.) | | |
| 8 | | Compendium of information | Watermark | 3/9/2005 |
| 9 | 98DX250, 98DX253, 98DX260, 98DX263, | Contains hardware specification for | and footer state | |
| | 98DX270, and | the Prestera product and provides the | "confidential" | |
| 10 | 98DX273 Prestera-DX | pinout, thermal characteristics and the electrical description of the device, | | |
| 11 | Specific pages that | such as power characteristics, voltage | | |
| 12 | contain purported trade secret | levels, and signaling technology that is used. | | |
| 13 | information: | The document discloses detailed power numbers on each voltage rail | | |
| | a. pp. 27-29, Table 4: | covering the worst-case capabilities of | | |
| 14 | Pin Functions and Assignment Table key, | the device, instead of a typical power consumption number often disclosed | | |
| 15 | Table 5: Tri-Speed | publically for marketing purpose. The | | |
| 16 | Network Ports Pin Descriptions, and | detailed power numbers enable customers to determine the cost of a | | |
| | Table 6: Hyper.GStack | system built around this device, while | | |
| 17 | PortsXAUI Interface; | they cannot achieve that result using just the typical power number. | | |
| 18 | b. pp. 68-70, Fig. 17: | Based on the information disclosed in this document, such as the pin map, | | |
| 19 | Pin Map Top View | the interface technologies Marvell | | |
| 20 | Left Side of the ball map for the device, | chose, the detailed power numbers and the worst-case capabilities of the | | |
| | Fig. 18: Pin Map Top | device, a competitor could make their | | |
| 21 | View Right Side of the ball map for the | own design choices to yield a device with more optimized design than | | |
| 22 | device, and Fig. 19, | Marvell's. | | |
| 23 | Table 20: Pinout Table; | Specific pages containing purported | | |
| 24 | c. pg. 95, section 7.5: | <u>trade secret information</u> | | |
| | Current Consumption; | a. Describes each interface and the | | |
| 25 | d. Government | pins associated with the interface, shows Marvell's choice of the SGMII | | |
| 26 | Exhibit 28: Email | as the signaling method for the Tri- | | |
| 27 | from Zhang to Kelly Coffey et al. on 6/2/05 | Speed Network Ports and its choice of XAUI interface for the 10 Gigabit | | |
| 28 | and attachment showing a portion of | ports, with this device being one of the first devices choosing XAUI as the 10 | | |

| Table 7.5.2 on Page 96 of Orgenment Exhibit 5. This Table specifies the AC/DC analog current consumption per active 1.25 Gbps SERDES lane. The consumption per active 1.25 Gbps SERDES lane. SERDES lane. The consumption per active 1.25 Gbps SERDES lane. The consumption of the current consumed by three different devices under worst-case operating voltages and the current consumed would be unique to Marvell's particular device and not known by the public. The consumption per active SERDES under extreme test conditions, with which one could calculate maximum power consumption per active SERDES. Sunder extreme test conditions, with which one could calculate maximum power consumption per active SERDES. Sunder extreme test conditions, with which one could calculate maximum power consumption per active SERDES, which is done through software. The power on and doven each SERDES, which is done through software. The power consumption values would allow competitors to understand what they need to target in order to effectively compete against Marvell. Government Exhibit 6: Prunctional Errata and Restrictions 98DX166, 98DX246, 98DX250, 98DX246, 98DX250, 98DX260, 98DX270 Specific pages that contain purported trade secret information: Government Exhibit 6: Prunctional Errata and Restrictions 98DX166, 98DX270 Specific pages that contain purported trade secret information to market against Marvell with a feature that Marvell cannot include. It would take competitors significant amount of time and research to find the errata on their own. Specific pages that containing purported trade secret information. Specific pages that containing purported trade secret information. Specific pages that containing purported trade secr | | | | | |
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| consumption per siven interface in an attempt to associate pins with a given interface in an attempt to reverse engineer would be extremely difficult. (It is not clear beyond a reasonable doubt that this specific information alone would have independent economic value from not being generally known.) 8 | | | device and its pin placement that aimed | | |
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| 98DX250, 98DX260 and 98DX270 Specific pages that contain purported trade secret information: 28 a. pg. 15, FEr #24: Aging Time of Storm Prevention Entries Aging is Not What is in the specification. Knowledge by a competitor would allow it to design test cases that could cause Marvell's chip to behave poorly or fail, or use the information to market against Marvell with a feature that Marvell cannot include. It would take competitors significant amount of time and research to find the errata on their own. Specific pages containing purported trade secret information | 21 | | | | |
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| 24 contain purported trade secret Marvell with a feature that Marvell cannot include. It would take competitors significant amount of time and research to find the errata on their own. 26 a. pg. 15, FEr #24: own. 27 Prevention Entries Aging is Not Specific pages containing purported trade secret information | 23 | | cause Marvell's chip to behave poorly | | |
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| 26 a. pg. 15, FEr #24: Aging Time of Storm Prevention Entries Aging is Not Specific pages containing purported trade secret information | 25 | information: | | | |
| 27 Prevention Entries Specific pages containing purported Aging is Not trade secret information | 26 | | | | |
| | 27 | Prevention Entries | | | |
| | 28 | | <u>trade secret information</u> | | |

| 1 2 | I | b. pg. 19, FEr #30: HyperG.Stack Port May Not be Able to | a. Describes a functional erratum with the storm prevention feature in Marvell's device, which is generally | | |
|-----|-----|--|---|----------------------------|----------|
| | | Operate at Speed of | used to reduce the overhead time for a | | |
| 3 | | 12Gbps. | CPU to process new addresses. The erratum is that when the storm | | |
| 4 | | | prevention feature is turned on, address aging is not accurate. With this | | |
| 5 | | | information, a competitor could build a test case to show that Marvell's storm | | |
| 6 | | | prevention feature is not usable. | | |
| 7 | | | b. Describes a functional erratum with the 10-Gigabit port: for certain packet | | |
| 8 | | | sizes, the ports are not able to run at 12 Gigabits/s though Marvell has so | | |
| 9 | | | specified. | | |
| 10 | | Government Exhibit 7: | Compendium of information | Watermark | 3/9/2005 |
| 11 | 5 | Functional Specification and | Describes, on a step-by-step basis, | and footer state | |
| 12 | | Register Set for Prestera 98DX166, | all the functions that are available for every packet or data that flows through | "confidential." Has second | |
| 13 | | 98DX166R, 98DX246, 98DX250, | the physical device, as well as configurable options associated with a | level of password | |
| 14 | Š | 98DX260, 98DX270, 98DX803, 98DX167, | certain function. It allows users to understand the theory of operation of | protection. | |
| 15 | Ģ | 98DX167R, 98DX247, 98DX253, | the device, stage-by-stage. | | |
| 16 | | 98DX263, 98DX273 | Specific pages containing purported trade secret information | | |
| 17 | | Specific pages that contain purported | a. Describes Marvell's new feature: | | |
| 18 | l t | trade secret information: | Secure Control Technology that is used to protect the CPU from a type of | | |
| 19 | | a. pg. 25: Secure | excessive network requests called Denial of Service Attacks. It also | | |
| | | Control Technology | shows the different mechanisms with | | |
| 20 | | (SCT) Features; | which this feature may work, and different control options Marvell | | |
| 21 | | b. pg. 252, section 12.2: Unicast Routing | provided to customers who use this feature. Public knowledge would | | |
| 22 | | Overview; | enable a competitor to understand what Marvell was offering to the customers | | |
| 23 | | c. pg. 607: Policers and Unicast Routing | in order to battle Denial of Service Attacks, which became a major threat | | |
| 24 | I | Engine Configuration | to networks in the 2003-2004 time frames. | | |
| 25 | | Registers. | | | |
| 26 | | | b. Describes a new routing feature being introduced to this class of | | |
| 27 | | | products. Section 12.2 and what follows give an overview of how | | |
| 28 | | | Marvell implements this feature, which did not exist in prior-generation | | |

| 1 | | solution, with only minimal changes to the predecessor chips. With this | | |
|-----|--|--|----------------------------------|-----------|
| 2 3 | | information, a competitor could copy Marvell's approach to provide the routing function to their own products. | | |
| 4 | | c. The table summarizes a list of | | |
| 5 | | registers and tables used to configure a rate-limiting mechanism within the | | |
| 6 | | device called Policer. Public knowledge would enable a competitor | | |
| 7 | | to understand the different controls associated with Policer and the limits of | | |
| 8 | | Policer's capability. | | |
| 9 | Government Exhibit 8: Functional Errata Register | Compendium of information Functional errata for the Prestera | Watermark and footer state | 3/9/2005 |
| 10 | | EX. Like Government Exhibit 6, this document describes the problems or | "confidential" | |
| 11 | 98EX128 and 98EX136 | change in operation of the actual silicon from what is in the | | |
| 12 | Specific pages that | specification. Knowledge by a competitor would allow it to design test | | |
| 13 | contain purported trade secret | cases that could cause Marvell's chip to | | |
| 14 | information: | behave poorly or fail, or use the information to market against Marvell | | |
| 15 | a. pg. 7, FEr #6 802.1p: | with a feature that Marvell cannot include. It would take competitors significant amount of time and research | | |
| 16 | Marking/Remarking Ignored on Bridged | to find the errata on their own. | | |
| 17 | | Specific pages containing purported trade secret information | | |
| 18 | b. pg. 15 FEr #23: | a. Describes a functional erratum with | | |
| 19 | | a feature that may improperly treat certain data packets that come in and | | |
| 20 | Multicast and Unknown Unicast | incorrectly mark them. Competitor could use this information to design a | | |
| 21 | Packets. | system or test lab to expose flaws that | | |
| 22 | | suggest Marvell not meeting industry standard. | | |
| 23 | | b. An errata describing the limitations | | |
| 24 | | of trunking distribution. Public knowledge could enable competitors to | | |
| 25 | | build a system to show poor performances of Marvell's products. | | |
| 26 | Government Exhibit 9: Application Note | Compendium of information | Watermark and footer | 3/16/2005 |
| 27 | Implementing a Solution Using Two | Describes a system implementation that uses one Prestera EX device and | state "confidential" | |
| 28 | Prestera Devices | one Prestera DX device, in the same | | |

| 1 | Considia no see that | system, thereby achieving cost | | |
|----|--|--|------------------|-----------|
| 2 | Specific pages that contain trade secret | efficiency. | | |
| 3 | information: | <u>Specific pages containing purported</u> <u>trade secret information</u> | | |
| | a. pg. 7: Desktop Line | | | |
| 4 | Card Features; | a. Shows positives and negatives from using this lost-cost two-device | | |
| 5 | b. pg. 8, section 5.2: | architecture. Specifically, section 5 describes the list of features that the | | |
| 6 | Ethernet Packet Marvell Tag Extension | architecture supports, and section 5.1 | | |
| 7 | for Cascading Prestera Packet Processors; | describes which features of the Prestera EX device are lost due to the use of this | | |
| | | architecture. Public disclosure would | | |
| 8 | c. pg. 10, section 6.1: Line Card Packet | allow a competitor to use the concept to achieve similar results or accentuate the | | |
| 9 | Flow. | negatives in competition. | | |
| 10 | | b. Describes how the proprietary TAG | | |
| 11 | | relates information between the two Prestera devices such that the EX | | |
| 12 | | device can take full advantage of the DX device capability. Public | | |
| | | knowledge would assist a competitor to | | |
| 13 | | design a similar type of system using their own TAG. | | |
| 14 | | c. Illustrates step-by-step how data | | |
| 15 | | packets flow through the system and | | |
| 16 | | how data from the DX device is mapped into the EX feature set. The | | |
| 17 | | latter is an important Marvell solution that allowed it to reduce the cost of the | | |
| | | system by fifty percent. Public | | |
| 18 | | knowledge would enable a competitor to employ similar approach to achieve | | |
| 19 | | fifty-percent cost reduction. | | |
| 20 | Government Exhibit | Compendium of information | Watermark | 3/16/2005 |
| 21 | 10: Prestera®- EX/MX/FX Hardware | Shows customers how to design a | and footer state | |
| 22 | Design Guide | board using Marvell's devices. It also shows certain Marvell design solutions | "confidential" | |
| | Specific pages that | such as the recommended layout of | | |
| 23 | contain purported trade secret | power balls and ground pins on a board. | | |
| 24 | information: | | | |
| 25 | a. pg. 23-24: Stackup | <u>Specific pages containing purported</u> <u>trade secret information</u> | | |
| 26 | Guidelines; | a. Describes the guidelines for how to | | |
| | b. pg. 27, Fig. 13: | limit the number of layers that the | | |
| 27 | PCB Cut Dimensions; | board requires when using Marvell's device. With this information, | | |
| 28 | c. pg. 28: Network | competitors who use similar interfaces | | |

| 1 | | Capacitor and Via | in their own chips could provide similar | |
|----|-----|------------------------------------|---|--|
| 2 | | Position –PS and CS View; | guidelines to their customers. (These stack-up guidelines are very | |
| 3 | | d. pg. 35, section 3.9: | specific to Marvell's products. It is hard to imagine that these guidelines | |
| 4 | | Frequent Layout Errors; | would have value to a competitor absent the remote possibility that its | |
| 5 | | e. pg. 85: Uplink | chips are substantially identical to Marvell's. No showing of the existence | |
| 6 | | Interface; | of such similarity has been made.) | |
| 7 | | f. pg. 92: Buffer SDRAM Interface. | b. Describes Marvell's recommendation to customers to use | |
| 8 | | SDIVINI Interface. | dual via connection in their board | |
| | | | design. Dual via connection is known technique in the industry. With this | |
| 9 | | | information, a competitor who uses similar types of interfaces with | |
| 10 | | | Marvell's product would be able to make similar recommendation to their | |
| 11 | | | customers. (Since dual via connection is known | |
| 12 | | | technique, it is not clear that a competitor using similar interfaces | |
| 13 | | | would need to look at this page to make similar recommendations to their | |
| 14 | | | customers. Therefore, no showing of trade secret status.) | |
| 15 | | | c. A picture of part of the actual board | |
| 16 | | | design, showing the placement of the power balls and how Marvell | |
| 17 | | | implements the dual via connection. | |
| 18 | | | The placement of the power balls and the ground pins is Marvell's solution to | |
| 19 | | | achieve a solid ground delivery as well as efficient power delivery. Public | |
| 20 | | | knowledge would enable a competitor to copy this solution without spending | |
| 21 | | | efforts to find it out on their own. | |
| 22 | | | d. Describes frequent errors that customers have made when using this | |
| 23 | | | chip. Describes why in some cases the industry standard might not be the best | |
| 24 | | | practice here. (Although one can speculate that | |
| 25 | | | this information would be useful, the evidence introduced does not establish | |
| 26 | | | that this information derives independent economic value from not | |
| 27 | | | being generally known.) | |
| 28 | | | e. Describes the uplink interface, a unique Marvell interface it shared with | |
| _0 | į l | | anique mai ven mientace it anarea with | |

| 1 2 | | its partners who build compatible devices. Public knowledge would enable competitors to understand the | | |
|--|--|--|-------------------------|-----------|
| | | concept and speed at which the | | |
| 3 | | interface runs; however, it does not necessarily enable them to use the | | |
| 4 | | information to an advantage. (Although this information may | | |
| 5 | | have independent economic value from not being generally known, the proof | | |
| 6 | | offered does not establish that beyond a reasonable doubt.) | | |
| 7 | | f. Describes Marvell's use of an | | |
| 8 | | industry standard interface for connecting to a graphic memory. It | | |
| 9 | | shows Marvell's design choice, | | |
| 10 | | particularly regarding the use of SDRAM rather than other types of DRAM. | | |
| 11 | | (Evidence is not convincing that | | |
| 12 | | information constitutes a trade secret as SDRAM is widely used in | | |
| 13 | | semiconductor chips.) | | |
| 1.4 | Government Exhibit | Compendium of information | Watermark | 3/16/2005 |
| 14 | 11: Application Note | | and footer | |
| | | Describes IPv4 Routing Engine data | | |
| 15 | AN-109 IPv4 Routing Engine Data Structure | Describes IPv4 Routing Engine data structure memory consumption in the | state "confidential" | |
| 15 16 | AN-109 IPv4 Routing Engine Data Structure Memory Consumption in the Prestera TM -EX | structure memory consumption in the Prestera-EX Packet Processor family. Compendium of information reveals in | state | |
| | AN-109 IPv4 Routing Engine Data Structure Memory Consumption | structure memory consumption in the Prestera-EX Packet Processor family. Compendium of information reveals in detail Marvell's unique hash algorithm for performing the routing look-up | state | |
| 16 | AN-109 IPv4 Routing Engine Data Structure Memory Consumption in the Prestera TM -EX Packet Processor | structure memory consumption in the Prestera-EX Packet Processor family. Compendium of information reveals in detail Marvell's unique hash algorithm | state | |
| 16 17 18 | AN-109 IPv4 Routing Engine Data Structure Memory Consumption in the Prestera TM -EX Packet Processor Family Specific pages that contain purported | structure memory consumption in the Prestera-EX Packet Processor family. Compendium of information reveals in detail Marvell's unique hash algorithm for performing the routing look-up functions as well as the performance of that algorithm under different network test cases. If competitors knew this | state | |
| 16 17 18 19 | AN-109 IPv4 Routing Engine Data Structure Memory Consumption in the Prestera TM -EX Packet Processor Family | structure memory consumption in the Prestera-EX Packet Processor family. Compendium of information reveals in detail Marvell's unique hash algorithm for performing the routing look-up functions as well as the performance of that algorithm under different network test cases. If competitors knew this information, they could use it in designing their own devices or to | state | |
| 16 17 18 19 20 | AN-109 IPv4 Routing Engine Data Structure Memory Consumption in the Prestera TM -EX Packet Processor Family Specific pages that contain purported trade secret information: a. pg. 2: Fixed Size | structure memory consumption in the Prestera-EX Packet Processor family. Compendium of information reveals in detail Marvell's unique hash algorithm for performing the routing look-up functions as well as the performance of that algorithm under different network test cases. If competitors knew this information, they could use it in designing their own devices or to design a test case that would cause Marvell's algorithm to behave poorly. | state | |
| 16 17 18 19 20 21 | AN-109 IPv4 Routing Engine Data Structure Memory Consumption in the Prestera TM -EX Packet Processor Family Specific pages that contain purported trade secret information: a. pg. 2: Fixed Size Initial Table; | structure memory consumption in the Prestera-EX Packet Processor family. Compendium of information reveals in detail Marvell's unique hash algorithm for performing the routing look-up functions as well as the performance of that algorithm under different network test cases. If competitors knew this information, they could use it in designing their own devices or to design a test case that would cause | state | |
| 16 17 18 19 20 21 22 | AN-109 IPv4 Routing Engine Data Structure Memory Consumption in the Prestera TM -EX Packet Processor Family Specific pages that contain purported trade secret information: a. pg. 2: Fixed Size Initial Table; b. pg. 3-4: Memory consumption tools & | structure memory consumption in the Prestera-EX Packet Processor family. Compendium of information reveals in detail Marvell's unique hash algorithm for performing the routing look-up functions as well as the performance of that algorithm under different network test cases. If competitors knew this information, they could use it in designing their own devices or to design a test case that would cause Marvell's algorithm to behave poorly. Most sensitive information of all the sixteen exhibits. Specific pages containing purported | state | |
| 16 17 18 19 20 21 22 23 | AN-109 IPv4 Routing Engine Data Structure Memory Consumption in the Prestera TM -EX Packet Processor Family Specific pages that contain purported trade secret information: a. pg. 2: Fixed Size Initial Table; b. pg. 3-4: Memory | structure memory consumption in the Prestera-EX Packet Processor family. Compendium of information reveals in detail Marvell's unique hash algorithm for performing the routing look-up functions as well as the performance of that algorithm under different network test cases. If competitors knew this information, they could use it in designing their own devices or to design a test case that would cause Marvell's algorithm to behave poorly. Most sensitive information of all the sixteen exhibits. Specific pages containing purported trade secret information | state | |
| 16 17 18 19 20 21 22 23 24 | AN-109 IPv4 Routing Engine Data Structure Memory Consumption in the Prestera TM -EX Packet Processor Family Specific pages that contain purported trade secret information: a. pg. 2: Fixed Size Initial Table; b. pg. 3-4: Memory consumption tools & | structure memory consumption in the Prestera-EX Packet Processor family. Compendium of information reveals in detail Marvell's unique hash algorithm for performing the routing look-up functions as well as the performance of that algorithm under different network test cases. If competitors knew this information, they could use it in designing their own devices or to design a test case that would cause Marvell's algorithm to behave poorly. Most sensitive information of all the sixteen exhibits. Specific pages containing purported | state | |
| 16 17 18 19 20 21 22 23 | AN-109 IPv4 Routing Engine Data Structure Memory Consumption in the Prestera TM -EX Packet Processor Family Specific pages that contain purported trade secret information: a. pg. 2: Fixed Size Initial Table; b. pg. 3-4: Memory consumption tools & | structure memory consumption in the Prestera-EX Packet Processor family. Compendium of information reveals in detail Marvell's unique hash algorithm for performing the routing look-up functions as well as the performance of that algorithm under different network test cases. If competitors knew this information, they could use it in designing their own devices or to design a test case that would cause Marvell's algorithm to behave poorly. Most sensitive information of all the sixteen exhibits. Specific pages containing purported trade secret information a. Describes the concept of the hash algorithm. Has independent economic value because knowledge of the | state | |
| 16 17 18 19 20 21 22 23 24 | AN-109 IPv4 Routing Engine Data Structure Memory Consumption in the Prestera TM -EX Packet Processor Family Specific pages that contain purported trade secret information: a. pg. 2: Fixed Size Initial Table; b. pg. 3-4: Memory consumption tools & | structure memory consumption in the Prestera-EX Packet Processor family. Compendium of information reveals in detail Marvell's unique hash algorithm for performing the routing look-up functions as well as the performance of that algorithm under different network test cases. If competitors knew this information, they could use it in designing their own devices or to design a test case that would cause Marvell's algorithm to behave poorly. Most sensitive information of all the sixteen exhibits. Specific pages containing purported trade secret information a. Describes the concept of the hash algorithm. Has independent economic value because knowledge of the algorithm would enable competitors to use the algorithm to obtain the results | state | |
| 16 17 18 19 20 21 22 23 24 25 | AN-109 IPv4 Routing Engine Data Structure Memory Consumption in the Prestera TM -EX Packet Processor Family Specific pages that contain purported trade secret information: a. pg. 2: Fixed Size Initial Table; b. pg. 3-4: Memory consumption tools & | structure memory consumption in the Prestera-EX Packet Processor family. Compendium of information reveals in detail Marvell's unique hash algorithm for performing the routing look-up functions as well as the performance of that algorithm under different network test cases. If competitors knew this information, they could use it in designing their own devices or to design a test case that would cause Marvell's algorithm to behave poorly. Most sensitive information of all the sixteen exhibits. Specific pages containing purported trade secret information a. Describes the concept of the hash algorithm. Has independent economic value because knowledge of the algorithm would enable competitors to | state | |

| 1 | | performance and efficiency of the hash algorithm. <i>See</i> (a) <i>above</i> on value. | | |
|------|---|---|----------------|-----------|
| 2 | Government Exhibit | Compendium of information | Footer states | 3/16/2005 |
| 3 | 12: Application Note | Compenatum of information | "confidential" | 3/10/2003 |
| | Prestera TM Packet | Describes which control tables exist | | |
| 4 | Processor Memory | in which memory and the potential | | |
| 5 | Usage | trade-offs that a system designer would have to make given a certain memory. | | |
| | Specific pages that | | | |
| 6 | contain trade secret | Specific pages containing purported | | |
| 7 | information: | <u>trade secret information</u> | | |
| | a. pg. 2-3: Internal | a. Describes the different components | | |
| 8 | Memory | that make up the internal memory of | | |
| 9 | Configuration & Tables Memory | Marvell's device. (Although this information may | | |
| | Consumption; | have independent economic value from | | |
| 10 | - | not being generally known, testimony | | |
| 11 | b. pg. 5: Flow Table; | presented did not establish that beyond | | |
| 11 | c. pg. 6-7: Flow | a reasonable doubt.) | | |
| 12 | Lookup Structure. | b. Describes the capabilities of the | | |
| 12 | | Marvell's flow table algorithm and its | | |
| 13 | | memory consumptions, without disclosing details of the algorithm. | | |
| 14 | | With this information, a competitor | | |
| | | may design a test case to cause | | |
| 15 | | Marvell's algorithm to fail. | | |
| 16 | | c. Describes memory requirements and | | |
| | | usage for different flow lookup | | |
| 17 | | structures. | | |
| 18 | | (As with (a) <i>above</i> , this information probably derives economic value from | | |
| 10 | | not being generally known but | | |
| 19 | | testimony presented did not establish | | |
| 20 | | that beyond a reasonable doubt.) | | |
| ∠U | Government Exhibit | Compendium of information | Footer states | 3/16/2005 |
| 21 | 13: Application Note | • • | "confidential" | |
| າາ ∥ | Software System Architecture for the 98 | Goes through how the software treats the value blade (a line card) that | | |
| 22 | EX126-98DX240 | provides enhanced services at lower | | |
| 23 | Value Blade | cost. | | |
| | | (The testimony offered did not | | |
| 24 | Specific pages that contain purported | establish independent economic value from not being generally known.) | | |
| 25 | trade secret | inom not being generally known.) | | |
| | information: | Specific pages containing purported | | |
| 26 | a na 2. Valva Dlada | <u>trade secret information</u> | | |
| 27 | a. pg. 2: Value Blade Architecture & System | a. A connection diagram showing how | | |
| | Benefits; | devices are connected in the value | | |
| 28 | | blade architecture, and the benefits of | | |

| 1 | 10 , | the architecture. | | |
|----|---|---|-------------------------|-----------|
| 2 | System Limitations; | b. Describes the features of a Marvell's | | |
| 3 | c. pg. 9, section 4.1: PSS Architectural | device that are lost due to the use of this architecture, in other words, the | | |
| 4 | Modifications | downside of using the architecture. Like Government Ex. 9, item a, public | | |
| 5 | | disclosure of this information could allow a competitor to accentuate the | | |
| 6 | | negatives in competition. | | |
| 7 | | c. Describes the architectural modifications Marvell made to the PSS | | |
| 8 | | (software driver for the Prestera Software Suite). | | |
| 9 | | (Testimony does not show beyond a reasonable doubt derivation of | | |
| 10 | | independent economic value from not being generally known.) | | |
| 11 | Government Exhibit 14: 88E1149 | Compendium of information | Watermark and footer | 3/18/2005 |
| 12 | Datasheet Integrated 10/100/1000 Gigabit | Datasheet for Marvell's quad-port Gigabit PHY. As PHY is an industry- | state "confidential" | |
| 13 | Ethernet Transceiver | standard interface, a large portion of this data sheet is specified according to | Commicman | |
| 14 | Specific pages that | industry standard. | | |
| | contain purported trade secret | (Although Exhibit 14 does show how Marvell implements a solution to | | |
| 15 | information: | meet those standards and that implementation may be a trade secret, | | |
| 16 | a. pp. 41-42, 48: Description of a power | the evidence presented is insufficient to show beyond a reasonable doubt the | | |
| 17 | management scheme using a feature called | information derives independent value from not being generally known.) | | |
| 18 | virtual cable testing. | Specific pages containing purported | | |
| 19 | | <u>trade secret information</u> | | |
| 20 | | a. Describes a low-power scheme of powering down output driver based on | | |
| 21 | | system characteristics. This is the mechanism Marvell uses to allow the | | |
| 22 | | device to meet industry standard while consuming less power. Public | | |
| 23 | | knowledge may allow a competitor to learn the mechanism and use it in their | | |
| 24 | | own products. | | |
| 25 | Government Exhibit 15: 98MX615, | Compendium of information | Watermark and footer | 3/18/2005 |
| 26 | 98MX625, 98MX635 Prestera® Packet | Datasheet for the Prestera MX device family. Like Government Ex. 1, | state "confidential" | |
| 27 | Processor Datasheet | here compendium of information shows the functional description of the device, | Companial | |
| 28 | Specific pages that | the interfaces on the device, the | | |

| 1 | contain trade secret | electrical power needed to power the | |
|----|-------------------------------|--|---|
| 2 | information: | device and specific design choices made by Marvell. Public disclosure of | |
| 3 | a. pg. 108: Reads and Writes; | information as a whole would enable competitors to gain unfair advantage | |
| 4 | b. pg. 110: Address | over products manufactured by Marvell as it allows competitors to understand | |
| 5 | Completion. | the theory of the device's operation that is unique to Marvell, as well as | |
| 6 | | Marvell's solutions in tackling certain challenges in this type of system | |
| 7 | | design. | |
| | | Specific pages containing purported | |
| 8 | | trade secret information | |
| 9 | | a. Describes in which part of the internal/external memory the Marvell | |
| 10 | | device stores certain data, and the options of reading some portions of the | |
| 11 | | memory with burst mode. Public knowledge could enable a competitor | |
| 12 | | to derive the structure of the internal memory in Marvell's device. | |
| 13 | | b. Describes a mechanism to address | |
| 14 | | the challenge when a CPU has to | |
| 15 | | access a much larger addressable memory inside a device than the PCI | |
| 16 | | bus that connects the CPU to the device. This solution allows system | |
| 17 | | designers to place multiple devices on the same PCI bus without allocating its | |
| 18 | | entire memory to any individual device. | |
| 19 | | | • |
| 20 | | | |
| 21 | | | |
| 41 | | | |

| 1 | Government Exhibit | Compendium of information | Watermark | 3/18/2005 |
|-----|---|---|----------------------|-----------|
| | 16: Application Note | D '1 M 11' | and footer | |
| 2 | Controlling Traffic to the CPU for 98EX1xx- | Describes Marvell's recommendation on how to control the | state "confidential" | |
| 3 | C/D and 98MXxx | traffic to the CPU in a complex system | Comidential | |
| | Prestera® Packet | built around Marvell's device that | | |
| 4 | Processors | receives multiple protocols. Public | | |
| 5 | Specific pages that | disclosure would reveal vulnerability of | | |
| 3 | contain purported | the system, enabling competitors to effectively bombard the system to make | | |
| 6 | trade secret | it fail or perform poorly. | | |
| _ | information: | | | |
| 7 | o na 10 Toblo 2 | Specific pages containing purported | | |
| 8 | a. pg. 10, Table 3: Handling Traffic to | <u>trade secret information</u> | | |
| Ŭ | CPU by Frame Type; | a. Detailed description of how Marvell | | |
| 9 | 10 5 11 | classifies data packets according to | | |
| 10 | b. pg. 13, Table 4 | different protocols. (Specific testimony explaining how | | |
| 10 | Sample Queue Usage for Traffic-to-CPU | the information derives economic value | | |
| 11 | Handling. | from not being generally known is | | |
| | | lacking.) | | |
| 12 | | h Marvall's recommended queue | | |
| 13 | | b. Marvell's recommended queue mapping. Although customers have | | |
| | | freedom to implement their own queue | | |
| 14 | | mapping, for those customers who do | | |
| 15 | | follow Marvell's recommendation, this table can be used to design an attack | | |
| 1.5 | | that makes the customers' system | | |
| 16 | | perform poorly. | | |
| | | | | |

12. At the time Zhang's residence was searched on June 24, 2005 for files downloaded from the Marvell Extranet, Zhang was questioned about the downloads by the case agent for the FBI. He acknowledged that he had downloaded Marvell files and first said that he was researching certain projects for Netgear and that was the reason that he downloaded the files. After being confronted with the fact that over seventy files had been downloaded and some did not appear to be related to Zhang's work at Netgear, he said that he was studying the files out of engineering curiosity. Zhang adamantly denied that he shared any of the information he downloaded with Broadcom or loaded any of it onto his Broadcom laptop computer. However, a subsequent search of his Broadcom laptop computer revealed that he had, in fact, copied the Marvell files he downloaded from the Marvell Extranet onto his Broadcom laptop. A June 2, 2005 e-mail shows that he

particular Marvell product over a competing one from Broadcom.

III. APPLICATION OF LAW TO FACTS

forwarded a page from his March 9, 2005 download (Exhibit # 28) to his colleagues at Broadcom

that tended to confirm that Cisco had not given Broadcom a truthful explanation as to why it chose a

A. Counts One Through Three (Computer Fraud and Abuse Act)

Counts One through Three allege violations of the Computer Fraud and Abuse Act ("CFAA"), 18 U.S.C. § 1030(a)(4), which imposes criminal liability on an individual who "knowingly, and with intent to defraud, accesses a protected computer without authorization, or exceeds authorized access, and by means of such conduct furthers the intended fraud and obtains anything of value" (Emphasis added). The CFAA does not define the term authorization. The term "exceeds authorized access" is, however, defined as "to access a computer with authorization and to use such access to obtain or alter information in the computer that the accesser is not entitled so to obtain or alter." 18 U.S.C. § 1030(e)(6). The Ninth Circuit in a recent en banc decision held that "exceeds authorized access' in the CFAA is limited to violations of restrictions on access to information, and not restrictions on its use." United States v. Nosal, 2012 WL 1176119 *8 (9th Cir. April 10, 2012) (emphasis in original); see also LVRC Holdings LLC v. Brekka, 581 F.3d 1127 (9th Cir. 2009).

In *Nosal*, the defendant had worked for Korn/Ferry, an executive search firm. Shortly after he left the company, he convinced some of his former colleagues who were still working for Korn/Ferry to help him start a competing business. The employees used their log-in credentials to download source lists, names and contact information from a confidential database on the company's computer, and then transferred that information to the defendant. The employees were authorized to access the database, but Korn/Ferry had a policy that forbade disclosing confidential information. The defendant was charged under the CFAA with violations of 18 U.S.C. § 1030(a)(4), for aiding and abetting the Korn/Ferry employees in "exceed[ing their] authorized access" with intent to defraud. The court held that the defendant had not violated the CFAA because the employees who acted on Nosal's behalf had authorized access to the database—the fact that they exceeded their authorized use did not give rise to a violation of the CFAA. *Nosal*, 2012 WL 1176119, at *7.

The parties were given leave to file supplemental briefing on the application of the holding in *Nosal* to the instant case because the en banc decision came out while the instant case was under submission. The government conceded in its supplemental brief that *Nosal* applies to the facts as alleged in the Superseding Indictment and as presented at trial in this case. The court agrees. Under the Ninth Circuit's definition of "exceeds authorized access," *id.*; *LVRC Holdings*, 581 F.3d at 1132-36, Zhang had "authorized access" to the Marvell Extranet when he downloaded the information from the Marvell Extranet in March 2005 because he had active log-in credentials at that time.

Therefore, Zhang is not liable for violating the CFAA. Specifically he is not guilty of Counts One through Three.

B. Counts Four through Six (Theft, Misappropriation and Unauthorized Downloading of Trade Secrets)

Zhang is charged in Counts Four through Six with misappropriating Marvell's trade secrets by his downloads from the Marvell Extranet on March 9, March 16 and March 18, respectively, in violation of 18 U.S.C. § 1832(a)(1), (2) and (4) which provide:

- (a) Whoever, with intent to convert a trade secret, that is related to or included in a product that is produced for or placed in interstate or foreign commerce, to the economic benefit of anyone other than the owner thereof, and intending or knowing that the offense will, injure any owner of that trade secret, knowingly—
 - (1) steals, or without authorization appropriates, takes, carries away, or conceals, or by fraud, artifice, or deception obtains such information;
 - (2) without authorization copies, duplicates, sketches, draws, photographs, downloads, uploads, alters, destroys, photocopies, replicates, transmits, delivers, sends, mails, communicates, or conveys such information;

* * *

(4) [or] attempts to commit any offense described in paragraphs (1) through (3) shall . . . be fined under this title or imprisoned not more than 10 years, or both.

In *Nosal* the Ninth Circuit pointed out how Congress used broader language than that used in the CFAA in describing the crimes set forth in 18 U.S.C. § 1832 "where it used the common law terms for misappropriation, including 'with intent to convert,' 'steals,' 'appropriates' and 'takes'." *Nosal*, 2012 WL 1176119, at *2 n. 3. Therefore, under § 1832, the elements the Government must have proven to establish Zhang's guilt are: (1) that Zhang intended to convert information to the

economic benefit of someone other than Marvell; (2) that the information constituted a trade secret; (3) that the trade secret was related to or included in a product that was produced for or placed in interstate or foreign commerce: (4) that Zhang stole or misappropriated the trade secret from Marvell; and (5) that he stole or misappropriated the trade secret intending or knowing that the offense would injure Marvell.

1. Zhang Intended to Convert the Information for His Own Benefit

The defense argued that Zhang had access to all the information that he downloaded and that the Government failed to prove that the downloads were not made by him in connection with his job duties as Netgear. The court finds that the circumstantial evidence establishes beyond any reasonable doubt that the subject downloads were not made by Zhang in the performance of his duties for Netgear. That circumstantial evidence includes:

- the timing of the downloads (they were accomplished after Zhang had accepted his position with Broadcom);
- the volume of Zhang's downloads after he had accepted the job offer from Broadcom but still at Netgear was substantially larger than the total downloads of all other Netgear employees;
- Zhang said that he had removed himself from any new Netgear projects before the downloads occurred;
- the downloads included information about products that Netgear had never purchased from Marvell;
- no evidence suggested that Zhang needed any of the information "to drive existing committed projects for delivery" (Ex. 31);
 - the downloaded material was transferred to Zhang's Broadcom laptop computer; and
- Zhang's responses to questions from the FBI agent at the time of the search of his premises strongly suggest that he was trying to hide the fact that he had downloaded the documents.

The court concludes beyond a reasonable doubt that Zhang intended to convert the downloaded material for his own personal benefit and not for the benefit of Netgear.

2. The Information Constituted Trade Secrets

[T]he term "trade secret" means all forms and types of financial, business, scientific, technical, economic, or engineering information, including patterns, plans, compilations, program devices, formulas, designs, prototypes, methods, techniques, processes, procedures, programs, or codes, whether tangible or intangible, and whether or how stored, compiled, or memorialized physically, electronically, graphically, photographically, or in writing if

- (A) the owner thereof has taken reasonable measures to keep such information secret; and
- (B) the information derives independent economic value, actual or potential, from not being general known to, and not being readily ascertainable through proper means by the public. . . .

18 U.S.C. § 1839; United States v. Chen, 659 F.3d 815, 827 (9th Cir. 2011).

As described in detail in the table above, the material downloaded by Zhang from the Marvell Extranet consisted of technical and engineering information, including compilations showing Netgear's design choices and implementations. It clearly falls within the type of subject matter that can be protected as a trade secret. *See* 18 U.S.C. § 1839 ("engineering information including . . . compilations, . . . designs"). However, to be considered a trade secret, the Government must have proven: (1) that Marvell took reasonable measures to keep such information secret; and (2) that the information Zhang downloaded derived independent economic value or potential economic value from not being generally known to, and not being readily ascertainable through proper means by the public. The Government relied on Exhibits 1 through 8 as showing trade secrets downloaded on March 9, 2005, Exhibits 9 through 13 as showing trade secrets downloaded on March 16, 2005 and Exhibits 14 through 16 as showing trade secrets downloaded on March 18, 2005. The evidence is first examined to determine whether the two requirements necessary for material to be considered trade secret are met.

a. Reasonable Measures to Keep Information Secret

The trade secret nature of information can be lost if reasonable measures are not taken to protect secrecy. In light of the nature of Marvell's business, Marvell could not restrict access to only a select group of its own employees. Existing and potential customers needed to review information concerning Marvell's products, including sensitive proprietary, confidential information, in order to consider the suitability of the Marvell chip sets for incorporation into their (the customer's or potential customer's) devices. The defense showed that some Marvell information that was marked

as "confidential" was freely available on the Internet from other than Marvell's website and that Broadcom had in its possession some "confidential" Marvell documents which were obtained from unidentified sources other than Zhang. None of the documents at issue, however, were found on the Internet or at Broadcom. Although Marvell's measures were not perfect and some unauthorized use of information obtained from Marvell was inevitable, Marvell's measures to maintain secrecy were reasonable, particularly given that Marvell's business required that trade secret information be shared with customers and potential customers. Marvell had an elaborate system that limited access to its Extranet to those with a need to know. See § 5(b)-(e) above. The system also allowed Marvell to trace who accessed the Extranet. Id. Documents that were downloaded were clearly marked as confidential. See § 5(e) above.

The defense did not challenge the security measures that restrict access to Marvell's business campus. It focused on what it contended is the inadequate protection of information available on the Marvell Extranet. Namoni Fine, an attorney whose practice involves advising clients on establishing procedures to protect trade secrets, was defendant's primary witness on the alleged inadequacy of Marvell's measures to maintain secrecy of its purported trade secrets. She was critical of Marvell for not being more discriminating in its labeling of its documents, i.e. marking as confidential or trade secret only those documents that clearly qualify as such. Ms. Fine contended that such overly liberal marking of information results in a lack of respect for confidential designations and a lack of understanding as to what documents truly contain trade secrets. Marvell did not distinguish documents containing trade secret information from those that Marvell wanted kept confidential even if they did not contain trade secret information. Although Ms. Fine did not identify particular documents or a class of documents that she believed were marked as confidential but, in fact, were not trade secrets, the evidence does suggest that Marvell marked everything available through the Marvell Extranet as "confidential." Although Marvell could have been more discriminating in its marking of documents, the evidence does not show that Netgear or any other customer was misled by, or failed to treat as secret, any Marvell document designated as confidential. In other words, Marvell's designation of some documents as confidential when they did not contain trade secrets,

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does not mean that Marvell lost trade secret status on those documents that actually contained trade secret information.

The court concludes that the Government proved that Marvell took reasonable measures to protect its trade secret information. Marvell was a company the industry recognized as being protective of its proprietary and confidential information. In light of Marvell's need to share information with actual and prospective customers, it went to elaborate lengths to restrict access to its Extranet and trace downloads from it. This, of course, does not mean that the marking of any document as proprietary and confidential makes it so. The document must still be proven to have trade secret status. In this case, we look primarily to Hervey's testimony and whether it established beyond a reasonable doubt the trade secret status of information in each of the sixteen documents. *See* Table in § 11 *above*.

Ms. Fine also advocated use of encryption techniques. She did not explain, however, how an encryption system could be utilized in Marvell's business nor the extent of any risk that could be eliminated by the use of encryption.

Ms. Fine also suggested that Marvell's security was deficient because Marvell did not maintain a practice of immediately revoking the Extranet password of any person whose employment with a company having access was terminated. Passwords had to be renewed every 120 days but Marvell did not have a practice of terminating access immediately on the departure of an accessing company's employee. However, since any log-in showed the date, time, name of the person logging on and nature of the download, a departing employee would know that any download he or she made in the limited time before his or her password expired could be traced. Under these circumstances, the risk of a breach of security by one whose password was about to expire was remote.

b. Potential Independent Economic Value From Not Being Generally Known

A majority of the documents introduced by the Government as Exhibits 1 through 16 were shown to each have independent value as compilations of information which showed Marvell's design choices and how Marvell implemented its designs and product features. *See* Table in § 11

above. Although much of the information in the downloaded documents is publicly available, the testimony of Hervey explained the benefit a competitor would gain from having the compilations. Hervey also identified several pages from the exhibits which contain information of actual or potential economic value from not being generally known. *See id*.

The court identified in § 11 *above* the material downloaded from each of the three charged downloads which was shown to have independent economic value from not being generally known to the public and not being readily ascertainable through proper means.

3. The Trade Secret Information Was Related to Various Products Placed in Interstate Commerce

There is no dispute that the trade secret information that Marvell sought to protect was information related to various products Marvell placed in interstate commerce, such as the Link Street line of products and the Prestera line, including the DX, EX and MX.

4. Zhang Stole the Trade Secret Information from Marvell

There is no dispute that Zhang downloaded the information which is the subject the indictment and, as discussed, the evidence showed that most of it was misappropriated.

5. Zhang Intended or Knew His Offense Would Injure Marvell

The only reasonable inference to draw from the evidence is that Zhang knew that his acquisition of the trade secret information would potentially injure Marvell. He, at a minimum, wanted to obtain the Marvell information so that he could become more knowledgeable and thus gain a competitive advantage over Marvell for himself, Broadcom or for whomever he was working at the time he saw fit to utilize what he had downloaded.

The Government has proved beyond a reasonable doubt that each element of the offense of theft of trade secrets under 18 U.S.C. § 1832 and, therefore, Zhang is guilty of Counts Four (theft of compendiums of information of Exhibits 1 through 8 and information on individual pages thereof identified as trade secrets in the Table in § 11 *above*), Count Five (theft of compendiums of information of Exhibits 9 through 13 and information on individual pages thereof identified as trade secrets in the Table in § 11 *above*) and Count Six ((theft of compendiums of information of Exhibits

13 through 16 and information on individual pages thereof identified as trade secrets in the Table in § 11 *above*).

C. Count Seven (Copying and Transmission of Trade Secrets)

Count Seven deals with Zhang's transmission on April 27, 2005 of the trade secrets he downloaded from the Marvell Extranet to his Broadcom laptop computer. 18 U.S.C. § 1832(a)(2) proscribes the unauthorized copying, duplicating or transmitting of trade secret information. The elements of this offense are discussed above. It is clear from the evidence that although Zhang denied dong so, a search of his Broadcom laptop computer revealed that on April 27, 2005, he copied the information he downloaded from the Marvell Extranet to his Broadcom laptop computer.

Zhang is guilty of Count Seven.

D. Count Eight (Unauthorized Transmission of Trade Secrets)

Count Eight charges that on June 2, 2005 Zhang transmitted Exhibit 28 (Tri-Speed Ports Table) to Kelley Coffee, a director of field application engineers at Broadcom, in violation of 18 U.S.C. §1832(a)(2). Zhang made the transmission in response to a concern Coffee had as to the truth of a purported explanation given by Cisco as to why it chose a Marvell device over a competing Broadcom device. The purported explanation was that the Broadcom device required "extra componentry" to interface with the Cisco device while the higher voltage Marvell device did not. Coffee did not think the explanation made sense and Zhang confirmed Coffee's belief that the explanation made no sense by sending him Exhibit 28, an excerpt from the Marvell DX 2X data sheet which showed that the Marvell device operated at the low voltage of a standard interface. This information was not secret. Coffee did not use the information to obtain any benefit for Broadcom except that he apparently blamed an employee in the Broadcom sales force for not having correct information about Marvell's standard interface operating voltage.

Although the electrical current information in Exhibit 28 constitutes trade secret information as discussed in § 11 *above*, Zhang did not transmit Exhibit 28 to Coffee for the purpose of disclosing the electrical current information. Since he sent only the one page from Exhibit 5, it appears that he intended only to answer Coffee's inquiry.

It is certainly possible, or even probable, however, that Zhang knew that Exhibit 28 contained electrical current data that was secret, proprietary information. However, it does not appear that he focused on that when he transmitted the document to Coffee. If he had wanted to reveal information about the hardware design of the DX 2X he presumably would have sent more information. There is no evidence that Broadcom was considering any redesign of its device or that the information on Exhibit 28 would have been of benefit if it did so. Coffee testified that the information provided no competitive advantage to Broadcom.

The court cannot find beyond a reasonable doubt that Zhang intended to, or knew, that the transmission would harm Marvell. Therefore, Zhang is not guilty of Count Eight.

E. Count Nine (Unauthorized Transmission of Trade Secrets)

Count Nine charges that on June 24, 2005 Zhang possessed the trade secrets he downloaded from the Marvell Extranet and transferred to his Broadcom laptop computer. His possession constitutes a violation of 18 U.S.C. § 1832(a)(2).

Zhang is guilty of Count Nine.

DATED: May 29, 2012

RONALD M. WHYTE
United States District Judge